

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicants : Vaughn Betz (as amended)
Application No. : 10/796,502 Confirmation No.: 3014
Filed : March 8, 2004
For : SYSTEMS AND METHODS FOR REDUCING
STATIC AND TOTAL POWER CONSUMPTION IN
PROGRAMMABLE LOGIC DEVICE
ARCHITECTURES
Art Unit : 2116
Examiner : Michael J. Brown

New York, New York 10036
March 26, 2007

Mail Stop AMENDMENT
Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

REPLY TO OFFICE ACTION

Sir:

In reply to the Office Action dated February 23, 2007, applicant hereby amends the above-identified patent application as follows:

Amendments of the Specification begin on page 2 of this Reply to Office Action.

Amendments of the Claims are reflected in the listing beginning on page 3 of this Reply to Office Action.

Amendments of the Drawings begin on page 23 of this Reply to Office Action and include two (2) attached replacement sheets.

Remarks begin on page 24 of this Reply to Office Action.

An **Appendix** including amended drawing figures is attached following page 27 of this Reply to Office Action.